



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/780,031 Confirmation No.: 9275
First Named Inventor: Park, Jongmin Filing Date: 17 February 2004
Group Art Unit: 2824 Examiner: Wendler, Eric J.
Atty. Docket No.: R-0003 US
Title: Technique for Programming Floating-gate Transistor Used in
Circuitry Such as Flash EPROM
Assignee(s): ProMOS Technologies Inc.

Mountain View, California
22 November 2005

**MAIL STOP RCE
COMMISSIONER FOR PATENTS
PO Box 1450
Alexandria, Virginia 22313-1450**

PRELIMINARY AMENDMENT

Sir:

Please amend the above patent application in the following manner.

Ronald J. Meetin
Attorney at Law
210 Central Avenue
Mountain View, CA
94043-4869
Tel.: 650-964-9767
Fax: 650-964-9779

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